ECE3120: Computer Systems Chapter 7: Interfacing with I/P Devices

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□ Prev

- Interfacing with Switches
- □ Today
 - Interfacing with Keypad

Interfacing to a Keyboard

- □ A keyboard is arranged as an array of switches,
 - mechanical
 - membrane
 - capacitors
 - Hall-effect in construction.
- □ Mechanical switches are most popular for keyboards.
 - Mechanical switches have a problem called contact bounce.
 Closing a mechanical switch generates a series of pulses because the switch contacts do not come to rest immediately.
 - In addition, a human cannot type more than 50 keys in a second. Reading the keyboard more than 50 times a second will read the same key stroke too many times.

Keypad Input Process

- A keyboard input is divided into three steps:
- Scan the keyboard to discover which key has been pressed
 - Debounce the keyboard to determine if a key is indeed pressed. Both hardware and software approaches for key debouncing are available.
 - Lookup the ASCII table to find out the ASCII code of the pressed key.

Keypad Scanning

- $\square PA7 \sim PA4 \rightarrow O/P, \text{ Row selection, row being } [(0,1,2,3),(4,5,6,7)..]$
- □ Row being scanned is driven low → either one of PA7~PA4=0
- □ PA3~PA0 \rightarrow I/P, Decide which key is pressed
 - Initially High , when pressed the corr row and column will be shorted
 - When pressed the corresponding PA Pin would be low

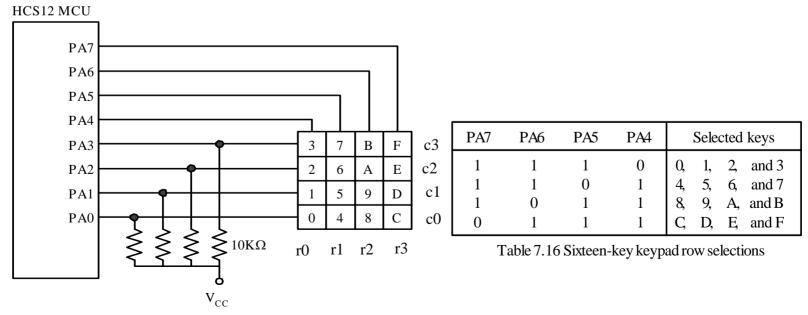


Figure 7.41 Sixteen-key keypad connected to the HCS12

Hardware Debouncing Techniques

Debouncer will recognize that the switch is closed after the voltage is low for around 10ms and that the switch is open after the voltage is high for about 10 ms

- □ SR latches
- Non-inverting CMOS gates
- Integrating debouncer

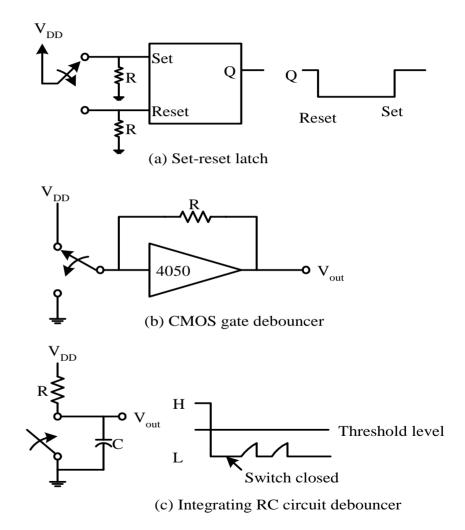


Figure 7.42 Hardware debouncing techniques

Software Debouncing Technique

- □ The most popular and simple one has been the **wait and see** method.
 - In this method, the program simply waits for about 10 ms and reexamines the same key again to see if it is still pressed.

- Example 7.10 Write a program to perform keypad scanning, debouncing, and returns the ASCII code in accumulator A to the caller.
- □ Solution
 - Pins PA4..PA7 each control one row of four keys.
 - Scanning is performed by setting one of the PA7...PA4 pins to low, the other three pins to high and testing one key at a time.

```
#include "c:\miniide\hcs12.inc"
keyboard equ PTA
```

get_char	movb	#\$F0,DDRA	; set PA7~PA4 for output, PA3~PA0 for input
scan_r0	movb	#\$EF,keyboard	; scan the row containing keys 0123
scan_k0	brclr	keyboard,\$01,key0	; is key 0 pressed?
scan_k1	brclr	keyboard,\$02,key1	; is key 1 pressed?
scan_k2	brclr	keyboard,\$04,key2	; is key 2 pressed?
scan_k3	brclr	keyboard,\$08,key3	; is key 3 pressed?
	bra	scan_r1	
key0	jmp	db_key0	
key1	jmp	db_key1	

key2 key3 scan_r1 scan_k4 scan_k5 scan_k6 scan_k7	brclr brclr brclr	db_key2 db_key3 #\$DF,keyboard ; scan the row containing keys 4567 keyboard,\$01,key4 ; is key 4 pressed? keyboard,\$02,key5 ; is key 5 pressed? keyboard,\$04,key6 ; is key 6 pressed? keyboard,\$08,key7 ; is key 7 pressed? scan r2
key4 key5 key6	jmp jmp jmp	db_key4 db_key5 db_key6
key7 scan_r2	jmp movb bclr	db_key7 #\$BF,keyboard ; scan the row containing keys 89AB keyboard,\$40 ; "
scan_k8 scan_k9 scan_kA scan_kB	brclr brclr brclr	keyboard,\$01,key8 ; is key 8 pressed? keyboard,\$02,key9 ; is key 9 pressed? keyboard,\$04,keyA ; is key A pressed? keyboard,\$08,keyB ; is key B pressed?
key8 key9	bra jmp jmp	scan_r3 db_key8 db_key9

keyA jmp db_keyA keyB jmp db_keyB #\$7F,keyboard movb ; scan the row containing keys CDEF scan r3 keyboard,\$01,keyC ; is key C pressed? scan_kC brclr keyboard,\$02,keyD; is key D pressed? scan kD brclr keyboard,\$04,keyE ; is key E pressed? scan kE brclr scan kF brclr keyboard,\$08,keyF ; is key F pressed? jmp scan r0 keyC jmp db_keyC keyD db_keyD jmp db_keyE keyE jmp db_keyF keyF jmp ; debounce key 0 delay10ms db_key0 jsr keyboard,\$01,getc0 brclr scan k1 imp #\$30 : return the ASCII code of 0 getc0 Idaa rts ; debounce key 1

db_key1 getc1	jsr brclr jmp Idaa	delay10ms keyboard,\$02,getc1 scan_k2 #\$31	; return the ASCII code of 1
	rts		
db_key2	jsr	delay10ms	
	brclr	keyboard,\$04,getc2	
	jmp	scan_k3	
getc2	ldaa rts	#\$32	; return the ASCII code of 2
db_key3	jsr	delay10ms	
	brclr	keyboard,\$08,getc3	
	jmp	scan_r1	
getc3	ldaa rts	#\$33	; return the ASCII code of 3
db_key4	jsr	delay10ms	
	brclr	keyboard,\$01,getc4	

	jmp	scan_k5	
getc4	ldaa	#\$34	; return the ASCII code of 4
	rts		
db_key5	jsr	delay10ms	
	brclr	keyboard,\$02,ge	tc5
	jmp	scan_k6	
getc5	Idaa	#\$35	; return the ASCII code of 5
-	rts		
dh low	lar	dalay 10ma	
db_key6	jsr	delay10ms	
ар_кеуб	jsr brclr	keyboard,\$04,ge	tc6
ар_кеуб			tc6
db_keyo	brclr	keyboard,\$04,ge	tc6 ; return the ASCII code of 6
	brclr jmp	keyboard,\$04,ge scan_k7	
getc6	brclr jmp Idaa	keyboard,\$04,ge scan_k7 #\$36	
	brclr jmp Idaa rts	keyboard,\$04,ge scan_k7 #\$36 delay10ms	; return the ASCII code of 6
getc6	brclr jmp Idaa rts jsr	keyboard,\$04,ge scan_k7 #\$36	; return the ASCII code of 6

getc7	ldaa rts	#\$37	; return the ASCII code of 7
db_key8	jsr brclr	delay10ms keyboard,\$01,getc8	
	jmp	scan_k9	
getc8	ldaa rts	#\$38	; return the ASCII code of 8
db_key9	jsr	delay10ms	
	brclr	keyboard,\$02,getc9	
	jmp	scan_kA	
getc9	Idaa	#\$39	; return the ASCII code of 9
	rts		
db_keyA	jsr	delay10ms	
	brclr	keyboard,\$04,getcA	
	jmp	scan_kB	
getcA	Idaa	#\$41	; get the ASCII code of A
	rts		
db_keyB	jsr	delay10ms	
	brclr	keyboard,\$08,getcB	
	jmp	scan_r3	
getcB	Idaa	#\$42	; get the ASCII code of B
	rts		

db_keyC	jsr brclr jmp	delay10ms keyboard,\$01,getc0 scan_kD	
getcC	ldaa rts	#\$43	; get the ASCII code of C
db_keyD	jsr brclr jmp	delay10ms keyboard,\$02,getcI scan_kE)
getcD	ldaa rts	#\$44	; get the ASCII code of D
db_keyE	jsr brclr jmp	delay10ms keyboard,\$04,getcl scan_kF	E
getcE	ldaa rts	#\$45	; get the ASCII code of E
db_keyF	jsr brclr jmp	delay10ms keyboard,\$08,getcl scan_r0	=
getcF	ldaa rts	#\$46	; get the ASCII code of F

delay10ms	movb	#\$90,TSCR1	; enable TCNT & fast flags clear
	movb	#\$06,TSCR2	; configure prescale factor to 64
	movb	#\$01,TIOS	; enable OC0
	ldd	TCNT	
	addd	#3750	; start an output compare operation
	std	TC0	; with 10 ms time delay
wait_lp2	brclr	TFLG1,\$01,wait_lp2	
	rts		

Next...

- □ Interfacing with LCD
- □ Time-Multiplexing
- □ Class in BN 320 on Friday Nov 7th
- □ Read Chapter 7.6